Serial Number: 09/800,703 Filing Date: March 7, 2001

Title: CHIP PACKAGE WITH DEGASSING HOLES

Assignee: Intel Corporation

REMARKS

The specification has been amended to update the priority information, specifically to include the patent number of the issued parent case. It is respectfully submitted that this change does not introduce new matter, and the claims are allowable without further search or consideration. Therefore, entry is appropriate under Rule 312, and is respectfully requested.

Respectfully submitted,

DUSTIN P. WOOD

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Cacia Lee

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Dkt: 884.159US2 (INTEL)

Attorneys for Intel Corporation

P.O. Box 2938

Minneapolis, Minnesota 55402

(612) 349-9581

My 17 04

Reg. No. 44,639

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this ______ day of August, 2004.